

Title: Method to Improve Profile Control and N/P Loading in Dual Doped Gate Applications
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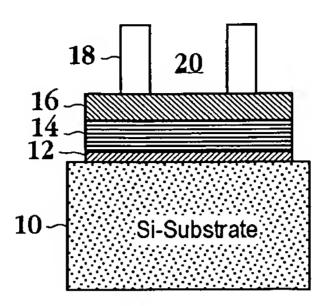


Fig. 1

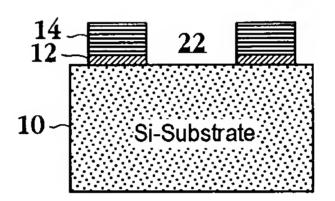


Fig. 2

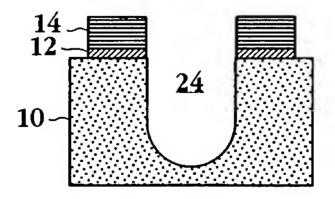


Fig. 3

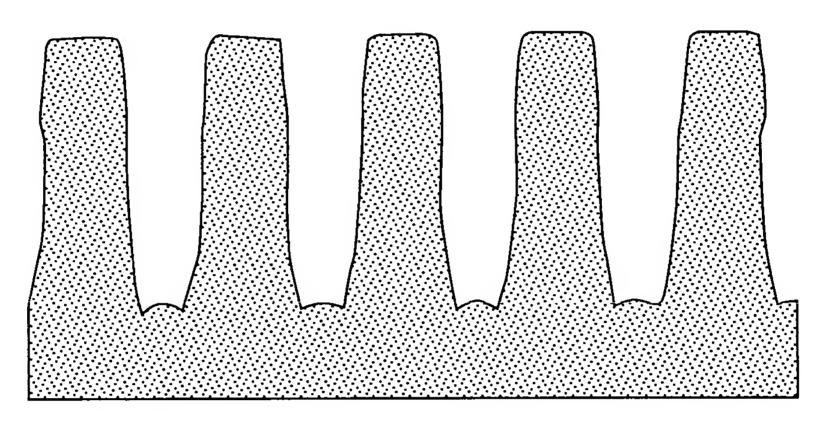


Fig. 4

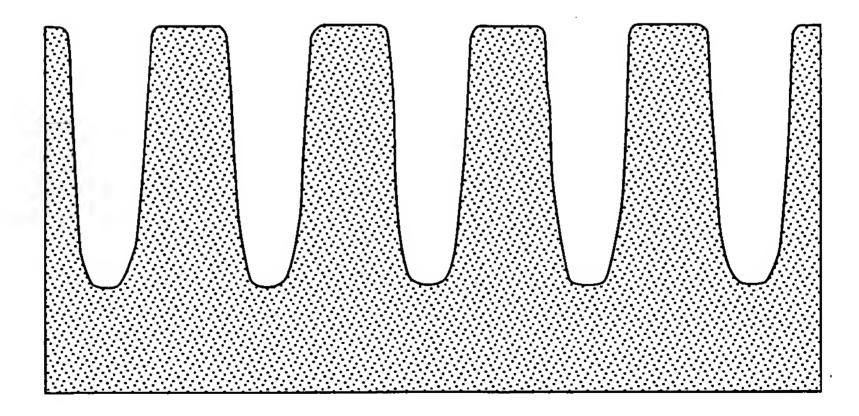


Fig. 5

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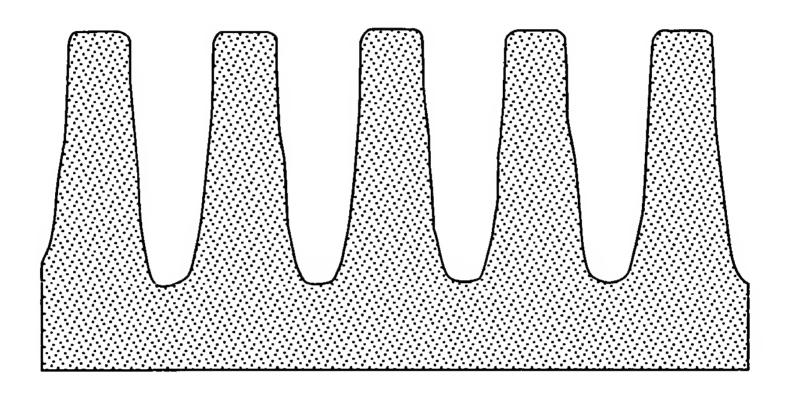


Fig. 6

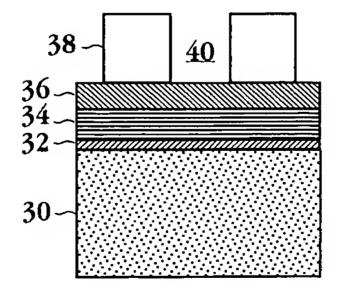


Fig. 7

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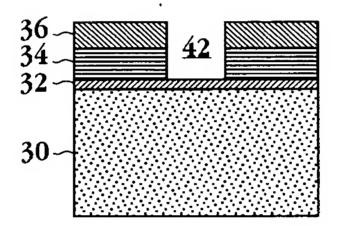


Fig. 8

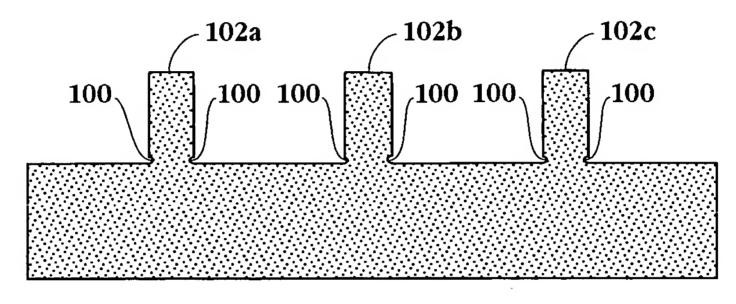


Fig. 9A

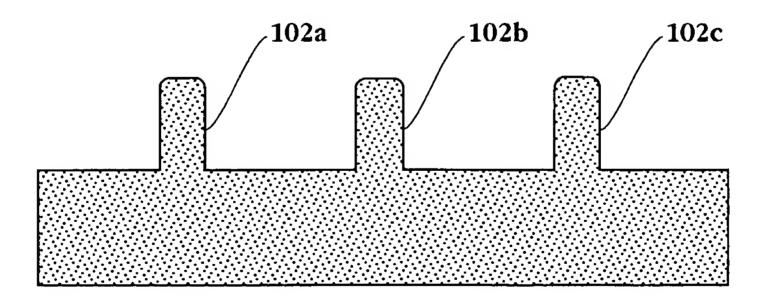


Fig. 9B

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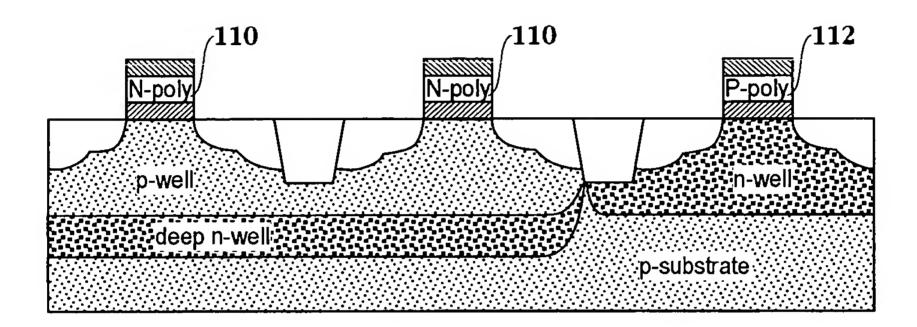


Fig. 10

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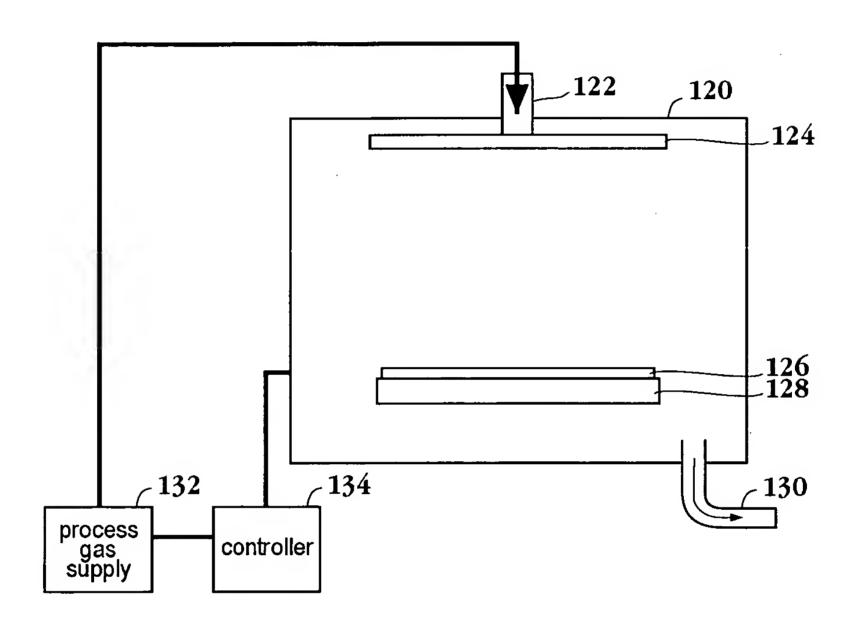


Fig. 11